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GATE
Practice Booklet

Bits & Bytes

VOLUME - I

ELECTRONICS & COMMUNICATION ENGINEERING

1116 EXPECTED QUESTIONS WITH SOLUTIONS

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Foreword

Dear Students,

Solutions of all previous GATE Questions are already available. Every year about 20% of questions will have repetitive nature. However, rest of the questions are from untapped areas (never asked areas) and few from Previous Engineering Services & Civil Services Questions. Keeping this in view, possible questions are prepared in various subjects (chapter wise) along with their hints/solutions. The student is advised to practice the questions systematically so that their chances of getting high score in GATE Exam will increase.

The student is advised to solve the problems without referring to the solutions. The student has to analyze the given question carefully, identify the concept on which the question is framed, recall the relevant equations, find out the desired answer, verify the answer with the final key such as (a), (b), (c), (d), then go through the hints to clarify his answer. This will help to face numerical answer questions better. The student is advised to have a standard text book ready for reference to strengthen the related concepts, if necessary. The student is advised not to write the solution steps in the space around the question. By doing so, he loses an opportunity of effective revision.

As observed in the GATE Exam, number of sets may be possible, being online exams. Hence, don't skip any subject. All are equally important.

It is believed that this book is a Valuable aid to the students appearing for competitive exams like IES, ISRO and Other PSU's. This book can also be used by fresh Teachers in Engineering in improving their Concepts.

With best wishes to all those who wish to go through the following pages.

Y.V. Gopala Krishna Murthy,
M Tech. MIE,
Chairman & Managing Director,
ACE Engineering Academy,
ACE Engineering Publications,
Frost Interactive Service Pvt. Ltd. (ACE ONLINE).

Syllabus for Electronics & Communication Engineering (EC)

Network, Signals and Systems:

Circuit analysis: Node and mesh analysis, superposition, Thevenin's theorem, Norton's theorem, reciprocity.
Sinusoidal steady state analysis: phasors, complex power, maximum power transfer.
Time and frequency domain analysis of linear circuits: RL, RC and RLC circuits, solution of network equations using Laplace transform.

Linear 2-port network parameters, wye-delta transformation.

Continuous-time signals: Fourier series and Fourier transform, sampling theorem and applications.
Discrete-time signals: DTFT, DFT, z-transform, discrete-time processing of continuous-time signals.
LTI systems: definition and properties, causality, stability, impulse response, convolution, poles and zeroes, frequency response, group delay, phase delay.

Control Systems:

Basic control system components; Feedback principle; Transfer function; Block diagram representation; Signal flow graph; Transient and steady-state analysis of LTI systems; Frequency response; Routh-Hurwitz and Nyquist stability criteria; Bode and root-locus plots; Lag, lead and lag-lead compensation; State variable model and solution of state equation of LTI systems.

Analog Circuits:

Diode circuits: clipping, clamping and rectifiers.
BJT and MOSFET amplifiers: biasing, ac coupling, small signal analysis, frequency response.
Current mirrors and differential amplifiers.
Op-amp circuits: Amplifiers, summers, differentiators, integrators, active filters, Schmitt triggers and oscillators.

Digital Circuits:

Number representations: binary, integer and floating-point- numbers.
Combinatorial circuits: Boolean algebra, minimization of functions using Boolean identities and Karnaugh map, logic gates and their static CMOS implementations, arithmetic circuits, code converters, multiplexers, decoders.

Sequential circuits: latches and flip-flops, counters, shift-registers, finite state machines, propagation delay, setup and hold time, critical path delay.

Data converters: sample and hold circuits, ADCs and DACs.

Semiconductor memories: ROM, SRAM, DRAM.

Computer organization: Machine instructions and addressing modes, ALU, data-path and control unit, instruction pipelining.

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